50DM DTO 4440	LLC DEDARTMENT OF COMMERCE	ATTORNEY DOCKET NO.	APPLICATION NO.	
FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	1454.1205		
		FIRST NAMED INVENTOR Georg DENK		
LIST OF REFER	ENCES CITED BY APPLICANT			
(Use se	veral sheets if necessary)	FILING DATE	GROUP ART UNIT	

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	/	DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA .	5,181,179	1/19/93	Fang et al.			
	AB						
	AC						
	AD						

FOREIGN PATENT DOCUMENTS

			DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSI YES	ATION NO
W A		AE	WO 98/24039	6/4/98	PCT				
	1	AF							
		AG							
		АН							
		ĄΙ							

OTHER REFERENCES (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.) Fröhlich et al., "A New Approach for Parallel Simulation of VLSI Circuits on a Transistor Level" (IEEE AJ <u>Transaction on Circuits and Systems - I: Fundamental Theory and Applications, Vol. 45, No. 6, June 1998, </u> Pages 601-613. AK Wever et al., "Parallel Transient Analysis for Circuit Simulation", Proceedings of the 29th Annual Hawaii International Conference on System Sciences, pages 442-447, 1996. Spiro, "Simulation Integrierter Schaltungen", 2 Auflage, R. Oldenbourg Verlag Munich, Wien 1990. Wever, et al., "Domain Decomposition Methods for Circuit Simulation", Proceedings of the 8th Workshop AM on Parallel and Distributed Simulation, PADS '94 Edinburgh, Scotland, UK, Pages 183-186, July 1994. Yilmaz E et al.: "Applying Globally Convergent Techniques to Conventional DC Operating Point Analyses", Proceedings 32nd Annual Simulation Symposium, San Diego, CA, USA, 11-15 April 1999, Pages 153-158, AN XP002173258, 1999, Los Alamitos, CA, USA, IEEE Comput. Soc, USA ISBN: 0-7695-0128-1, Paragraphs '02.2!, "03.2!, Figures 1 and 6.

	/ /	ــــــــــــــــــــــــــــــــــــــ					
EXAM	INE				DA	ATE CONSIDERED	
	$\mathcal{J}(i)$	Q	\rightarrow			26-AFTEM	BER - 2023.
				dered, whether or not citation		ance with MPEP 609; Drawline through cition to applicant.	tation if not in
£							